

Title : METHOD AND ARRANGEMENT FOR CHANGING PARALLEL CLOCK SIGNALS IN A DIGITAL DATA TRANSMISSION

## Appendix

### Claims:

1. A method for ~~changing parallel signals in a digital data transmission over a radio link, in which data flow to be transmitted is divided into several transmissions, comprising the steps of:~~

selecting a primary transmission path;

calculating a check sum for the data flow of a length of a processed section of the data flow, said check sum being added to the processed section of the data flow in order to form a data frame to be transmitted;

transmitting the data frame in at least two transmission paths that include the primary transmission path;

correcting correctable errors in received data frames and calculating an error sum for each of the at least two transmission paths;

comparing the error sum of ~~a selected one of the transmission paths~~ with an error sum of another of the at least two transmission paths, said transmission path being changed to a and changing to the transmission path with a the smaller error sum when said transmission path is selected as the path to be received;

changing ~~a path of a clock signal to the transmission path with the smaller error sum~~ after waiting for sufficiently accurate cophasal clock signals; and

forwarding ~~information in the data flow~~ data of the processed section of the data flow from the selected transmission path with the smaller error sum to an output cable.

2. The method according to claim 1, wherein the check sum is calculated by multiplying the data flow by a polynome suitable for modelling.

3. (Canceled)

4. An indoor unit ~~for digital data transmission and for selecting a data flow for parallel~~

~~signals in a digital data transmission over a radio link~~, comprising:

at least a changeover device for receiving and changing a propagation assured signal on a basis of an error sum obtained from an outdoor unit;

wherein said changeover device is configured to change clock signals after waiting for sufficiently accurate cophasal clock signals; and

wherein the changeover device comprises:

a multiplexer for receiving the clock signals of signal pairs to be received and for selecting one of the clock signal signals to be received;

data frame decoding blocks for receiving the clock signals and data signals and for forming ~~said signals~~ them into control signals and data signals which are decoded from the data frames;

elastic buffer and control blocks for receiving the control signals and data signals decoded from the data frames and for receiving ~~a the~~ selected clock signal in order to synchronize the data signals decoded from the data frames;

a data signal multiplexer for receiving the synchronized data signals from the elastic buffer and the control blocks; and

a decoding block for receiving ~~a at least one of the synchronized data signal signals~~ from the data signal multiplexer and for controlling the data signal multiplexer.

5. The indoor unit according to claim 4, wherein the indoor unit comprises part of a radio link in a mobile telecommunications system.

6-9. (Canceled)

10. An apparatus ~~for changing parallel signals in a digital data transmission over a radio link~~, said apparatus comprising:

a first indoor unit for dividing data which flows over ~~the a~~ radio link, said first indoor unit having a first changeover device for receiving propagation assured data;

antennas for transmitting and receiving parallel clock signals; and

a second indoor unit for selecting ~~the a~~ data flow of the divided

data, said second indoor unit having a second changeover device for receiving the propagation assured data;

wherein said changeover devices are configured to change clock signals after waiting for sufficiently accurate cophasal clock signals, and the first and second outdoor units are ~~provided with~~ comprise means for processing data to be transmitted using an algorithm that models the data to be transmitted, checks the data to be received and corrects errors in the data,

wherein each of the changeover devices comprise:

a multiplexer for receiving the clock signals of signal pairs to be received and for selecting one of the clock signals ~~signal to be received~~,

data frame decoding blocks for receiving the clock signals and data signals and for forming ~~said signals~~ them into control signals and data signals which are decoded from the data frames,

elastic buffer and control blocks for receiving the control signals and data signals decoded from the data frames and for receiving the selected clock signal in order to synchronize the data signals decoded from the data frames,

a data signal multiplexer for receiving the synchronized data signals from the elastic buffer and control blocks, and

a decoding block for receiving ~~a one of the~~ synchronized data signal from the data signal multiplexer and for controlling the data signal multiplexer.

11.(Canceled)

12. The apparatus according to claim 10, wherein the apparatus comprises part of a radio link in a mobile telecommunications system.